

***** Device Pin Out

Device : XC9572-15-PC44

	C	S	C	S	C	S	C	S	C			
	A	C	A	A	A	A	A	A	V	A		
	L	L	L	L	L	L	L	L	C	L		
	4	K	5	5	6	6	7	7	8	C		

	/6	5	4	3	2	1	44	43	42	41	40	\
SAL4	7										39	SAL9
CAL3	8										38	CAL9
SAL3	9										37	SAL10
GND	10										36	CAL10
CAL2	11										35	SAL11
SAL2	12										34	CAL11
CAL1	13										33	SAL12
SAL1	14										32	VCC
TDI	15										31	GND
TMS	16										30	TDO
TCK	17										29	CAL12
	\ 18	19	20	21	22	23	24	25	26	27	28	/

	P	P	S	V	C	G	S	C	S	C	S	
	G	G	A	C	A	N	A	A	A	A	A	
	N	N	L	C	L	D	L	L	L	L	L	
	D	D	1		1		1	1	1	1	1	
			6		5		5	4	4	3	3	

Legend : PGND = Tie pin to GND for additional ground path or leave unconnected

- VCC = Dedicated Power Pin
- GND = Dedicated Ground Pin
- TDI = Test Data In, JTAG pin
- TDO = Test Data Out, JTAG pin
- TCK = Test Clock, JTAG pin
- TMS = Test Mode Select, JTAG pin
- PROHIBITED = User reserved pin